



Faculty of Engineering
DEPARTMENT of ELECTRICAL AND ELECTRONIC
ENGINEERING

EENG447
Digital IC Design

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Micro Project

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Good Luck

Arithmetic Unit:

Arithmetic unit shown in Figure 2, performs addition, subtraction and negate (2's complement) operation of two numbers (A and B) using an 8-bit ripple carry adder and an 8-bit negate block which consists of an 8-bit 2-to-1 multiplexer and an 8-bit ripple carry adder. Each of these blocks is described in the following sections.

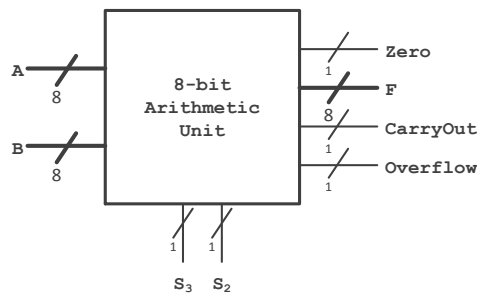


Figure 1: Arithmetic Unit Symbol.

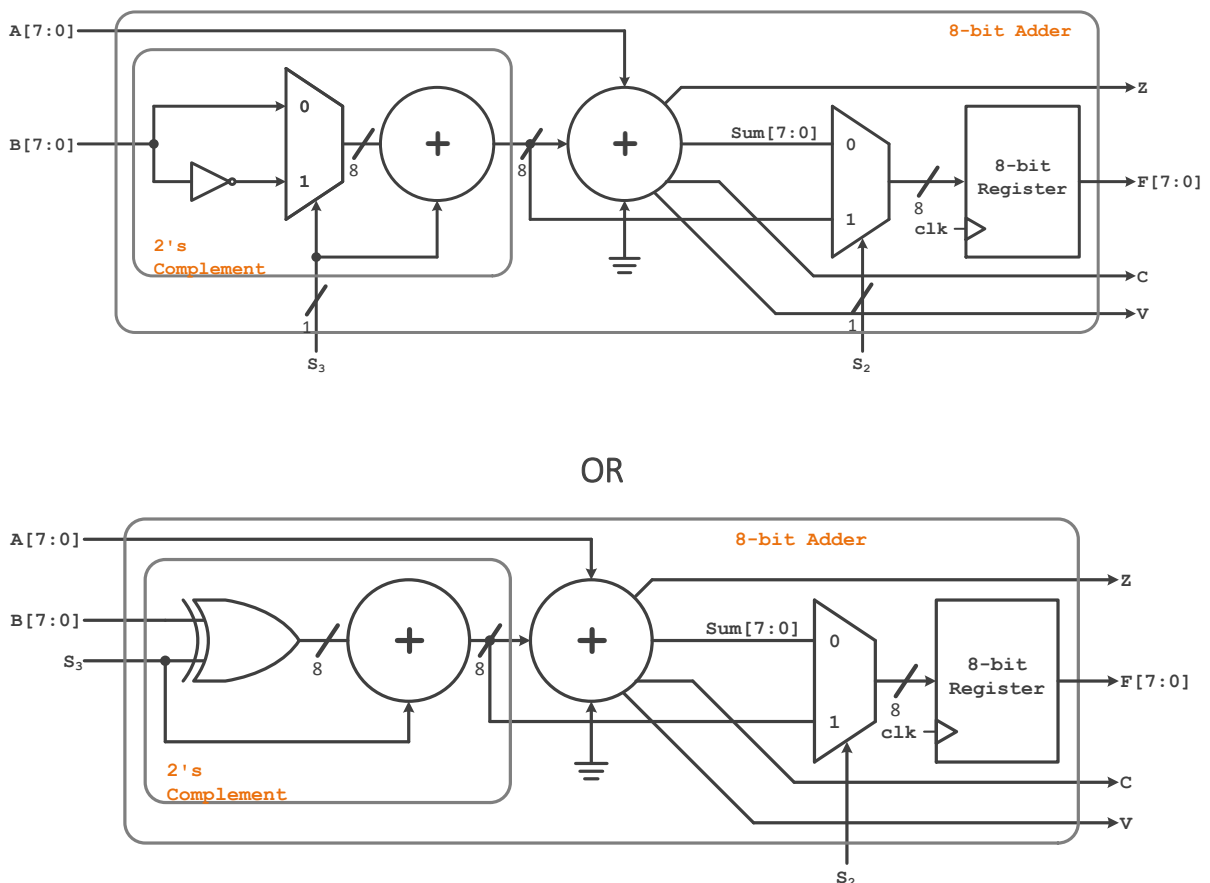


Figure 2: Arithmetic Unit Architecture.

