EENG447 Reminders!

Lectures:

Monday, 18.12.17 – The lecture on Physical Layout completed.

Thursday, 21.12.17 – In the first 45mins there will be a Quiz on stick diagrams. Then the lecture on Testing ICs will be covered. (This will be the last topic before the exams).

Monday 25.12.17 – The lecture hours will be used as lab hours, to work on your project, attendance will be taken. This will take place in SIMLAB.

Thursday 28.12.17 – The lecture hours will be used as lab hours, to work on your project, attendance will be taken. This will take place in SIMLAB.

Labs:

Thursday, 21.12.17 – On this week's lab, you will be assessed from ALU schematics. You have to complete and show all the schematics and simulations that are needed for 8-bit ALU. This is 7.5%.

Thursday, 28.12.17 – On the next week's lab, you will be assessed from 1-bit Arithmetic Unit Layout. You have to complete the physical layout, it should be DRC checked and LVS done. This is 7.5%.

Quiz:

This Thursday 21.12.17 you will have a quiz on stick diagrams. This is 5%. Do not forget to bring your coloured pens!

Project:

Please check the module website for the details. There is a new document called "Micro Project". For the submission of your project, you have to complete only:

- Schematic Design of an 8-bit Arithmetic Unit without comparator.
- Spice simulations for an 8-bit Arithmetic Unit without comparator.
- Layout Design of an 8-bit Arithmetic Unit without comparator.
- DRC check for an 8-bit Arithmetic Unit without comparator.
- LVS check for an 8-bit Arithmetic Unit without comparator.
- Report need to be submitted with all the details of the design.

Good Luck!

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