



EASTERN MEDITERRANEAN UNIVERSITY

**DEPARTMENT OF ELECTRICAL AND ELECTRONIC
ENGINEERING**

EENG342 ELECTRONICS II

Fall 2019

EXPERIMENT 4

INTEGRATOR

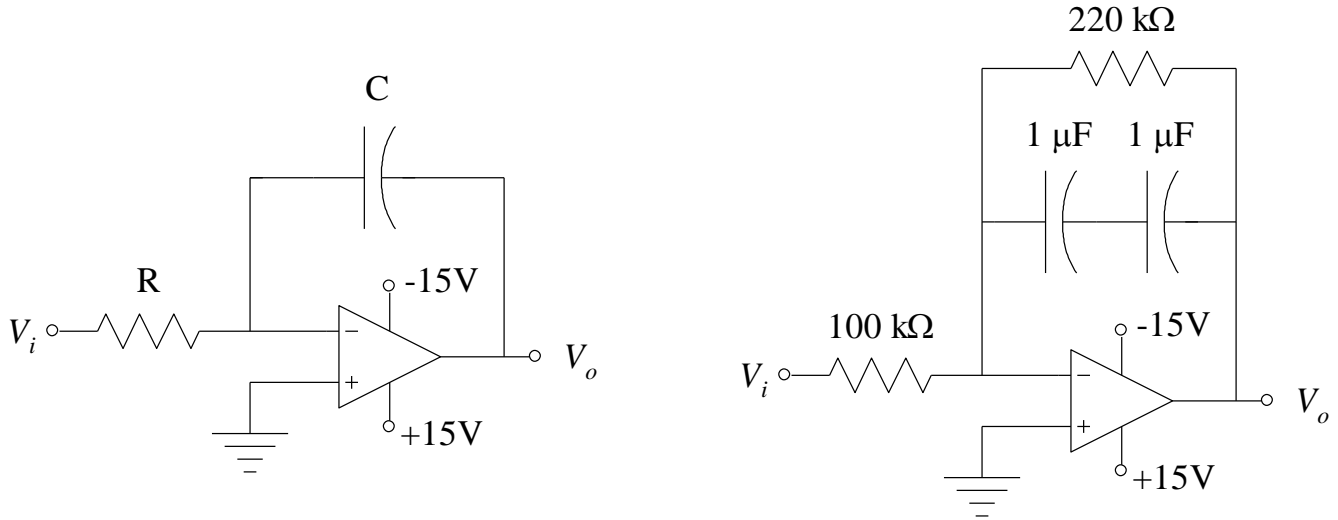
Name	Std. No.

Group No. :

Date:

Object: An understanding of the behavior of an integrator.

Step 1: Construct the circuits shown below.



Oscilloscope Settings:

CH 1 (V_i)	: 2 VOLTS/DIV	AC Coupling
CH 2 (V_o)	: 1 VOLTS/DIV	AC Coupling
TIME	: 0.5 m SEC/DIV	
0- REFERENCE	: middle horizontal line	

Step 2: Apply 4 V_{p-p} squarewave (1 KHz) as an input signal.

Step 3: Using the given oscilloscope settings sketch and label V_i and V_o to the provided graph paper.

$R = 1\text{ K}\Omega$

$C = 1\text{ }\mu\text{f}$

QUESTIONS:

Q1) What is the shape and peak-to-peak value of V_o ?

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Op amp integrator wave forms

